

FIG. 1  
(PRIOR ART)

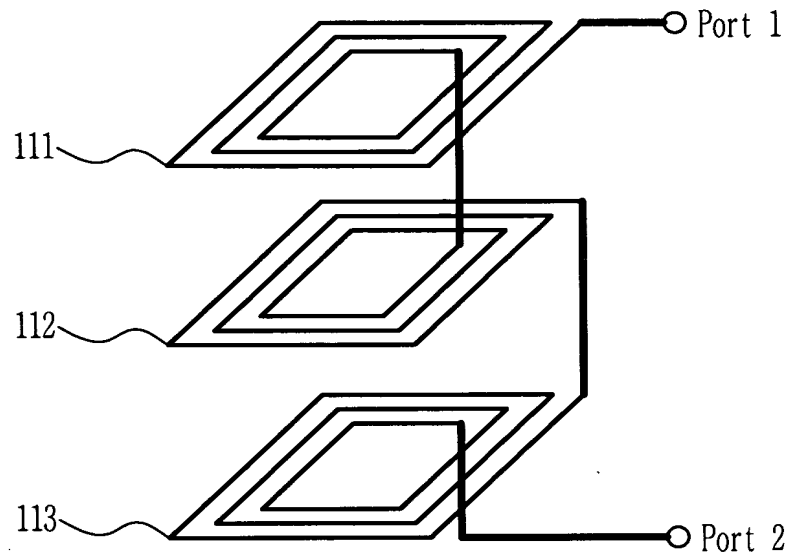
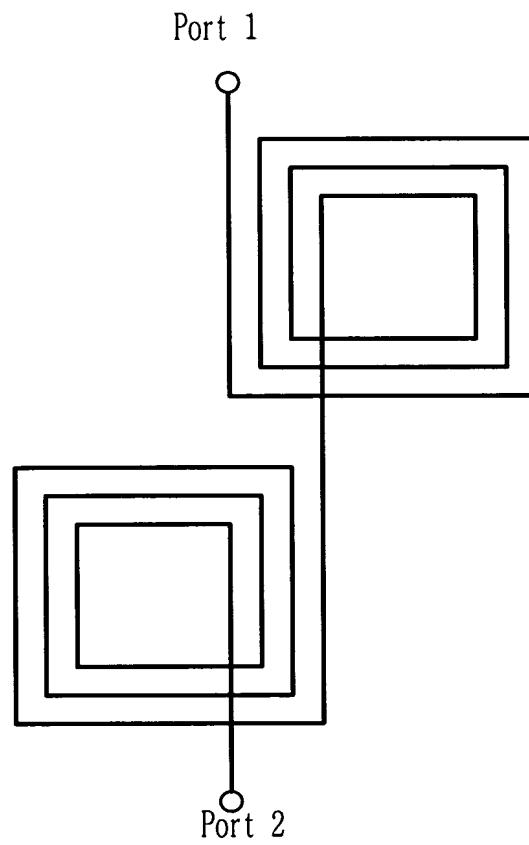


FIG. 2  
(PRIOR ART)



A circuit diagram showing a parallel combination of a resistor  $R$  and a capacitor  $C$  in series with a dependent current source  $g_m$ . The output voltage is labeled  $L=?$ .

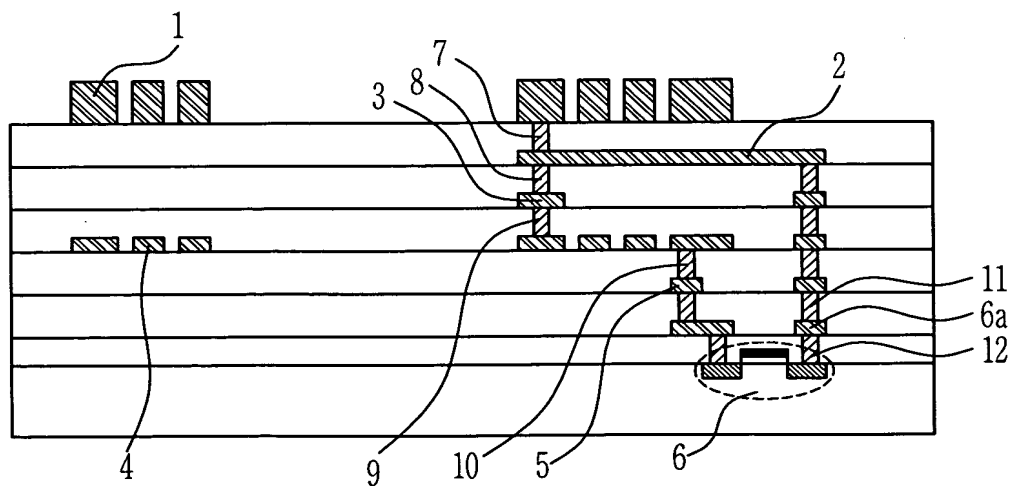
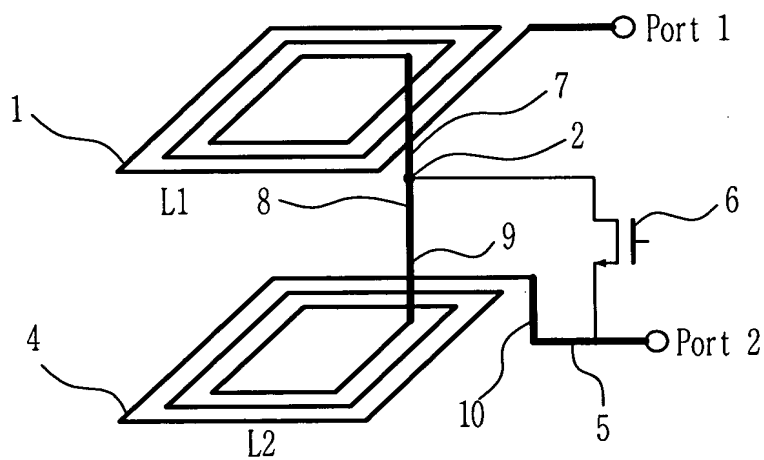
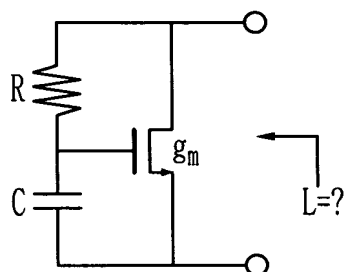


FIG. 6A

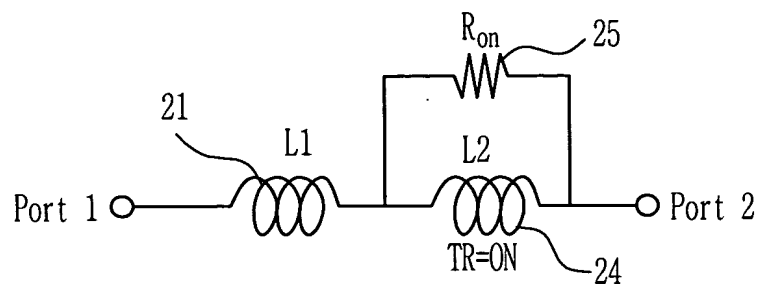


FIG. 6B

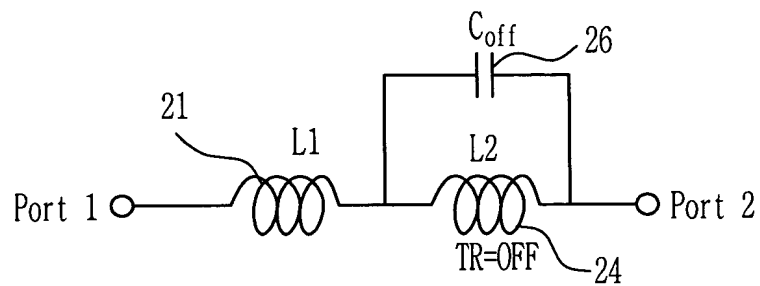


FIG. 7

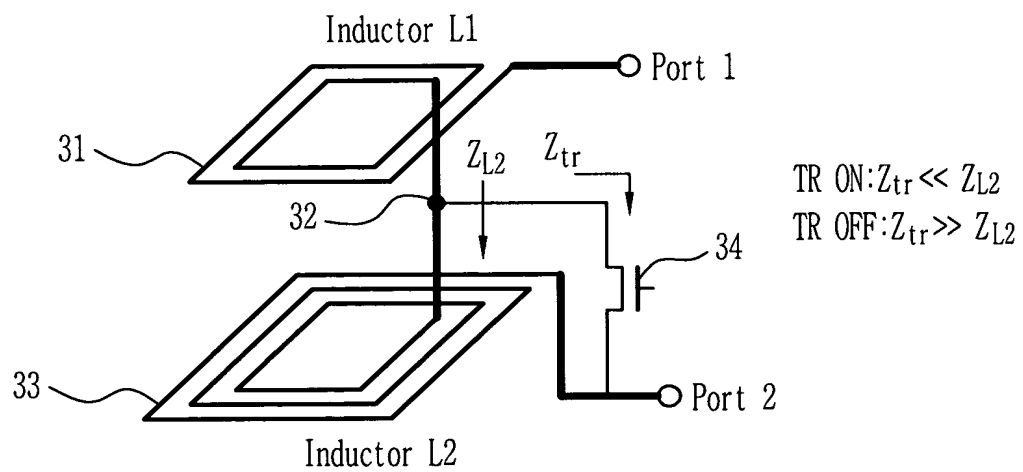


FIG. 8

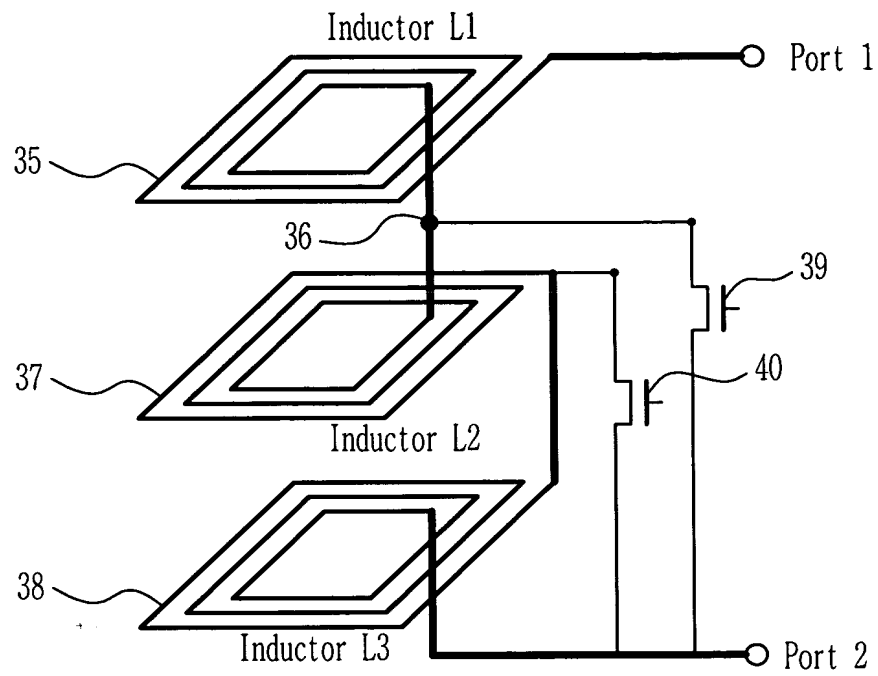
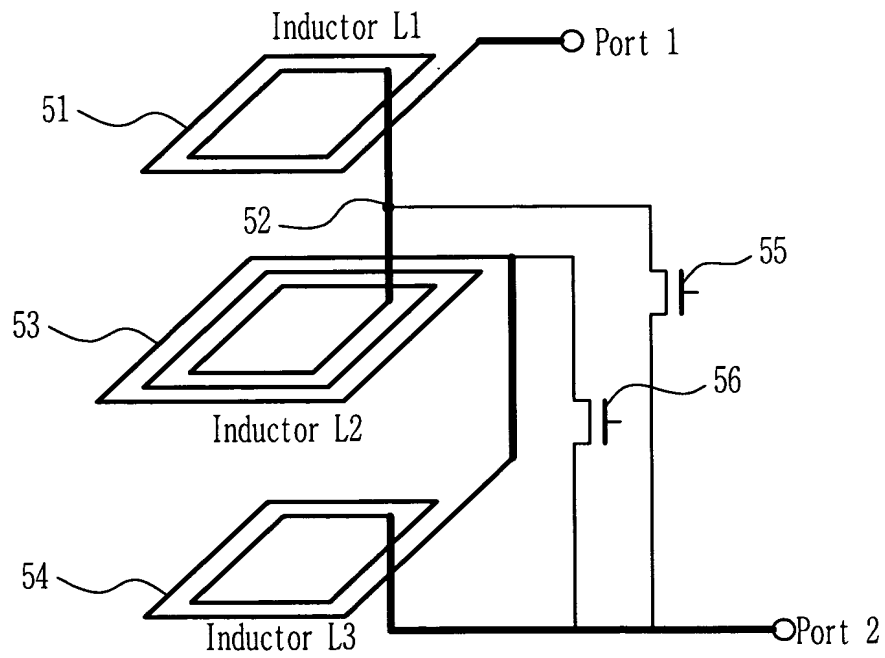


FIG. 9A



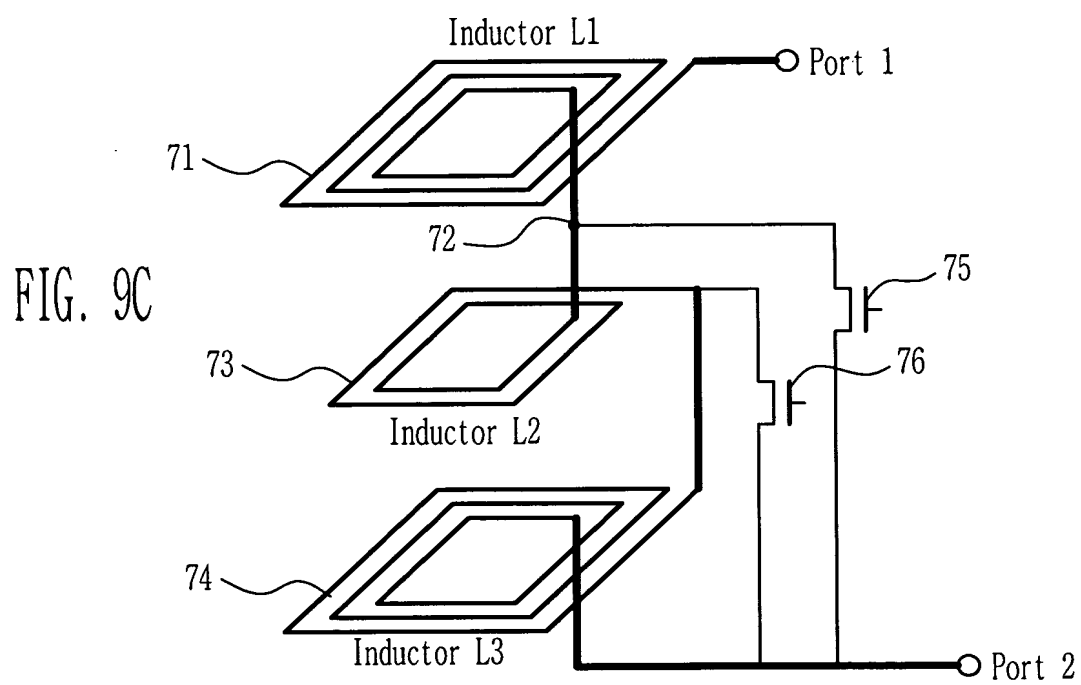
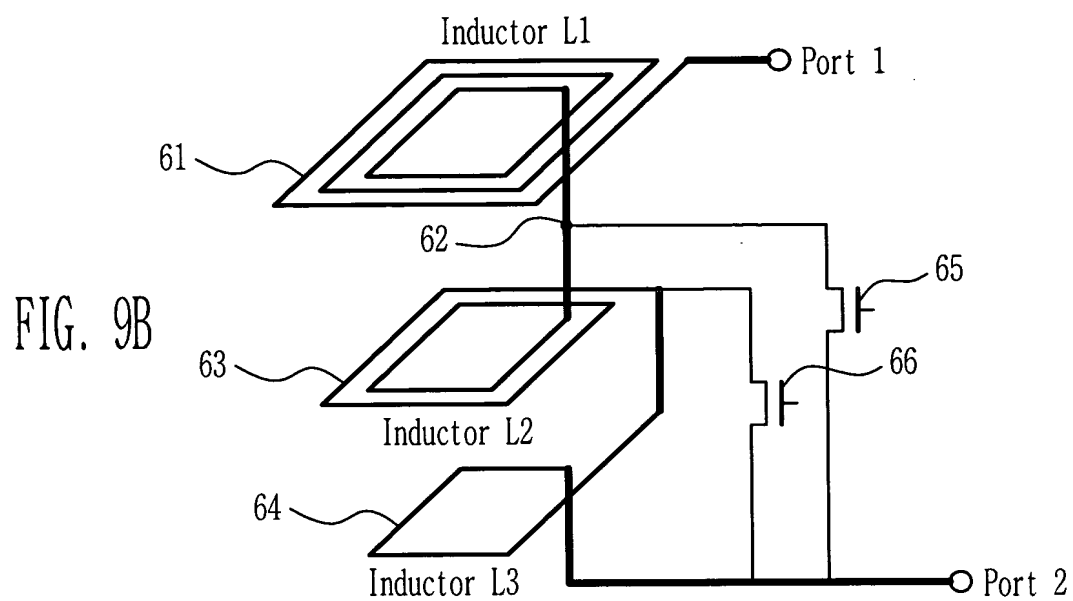


FIG. 9D

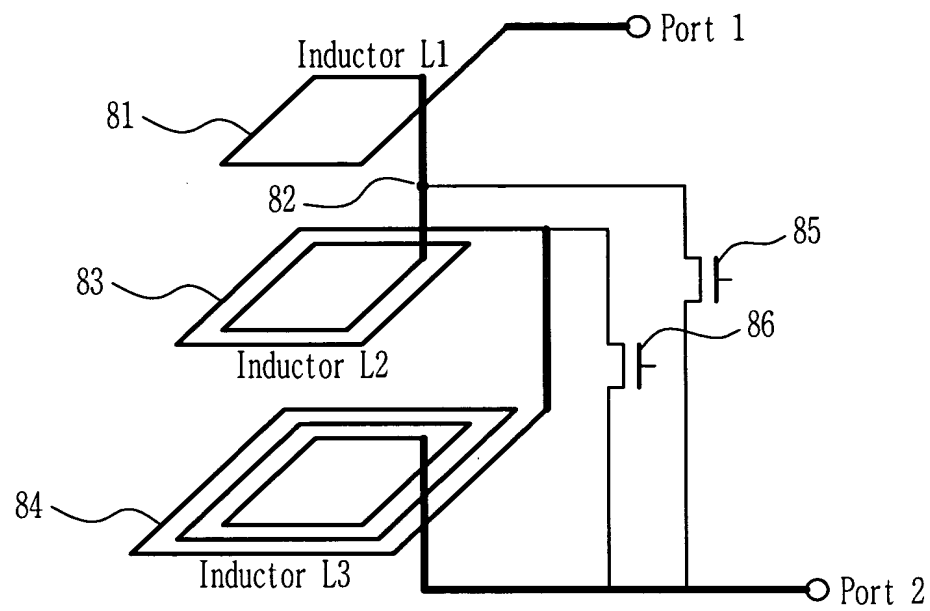


FIG. 10

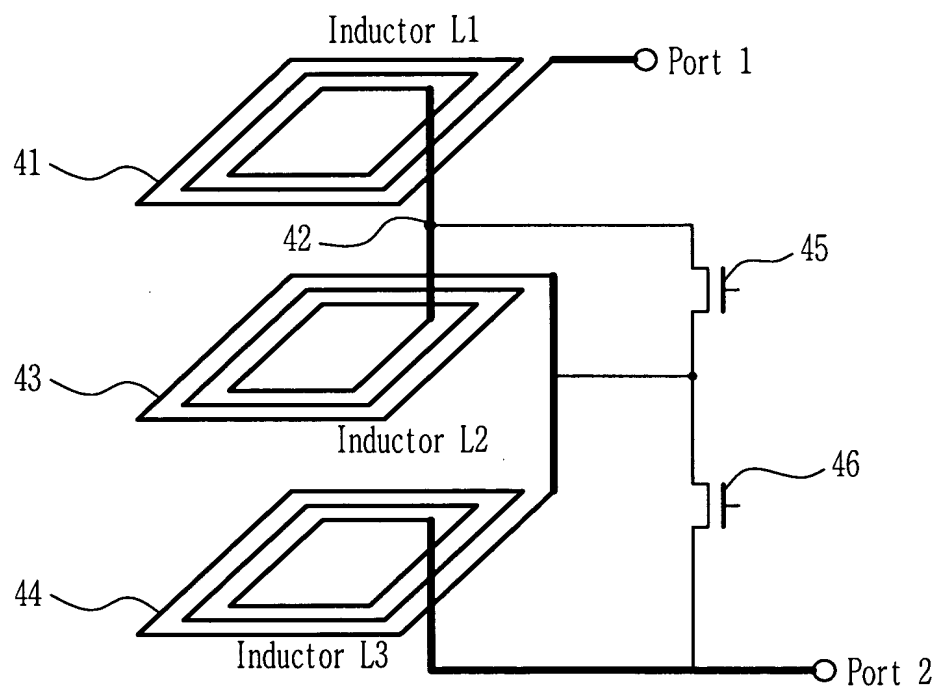


FIG. 11

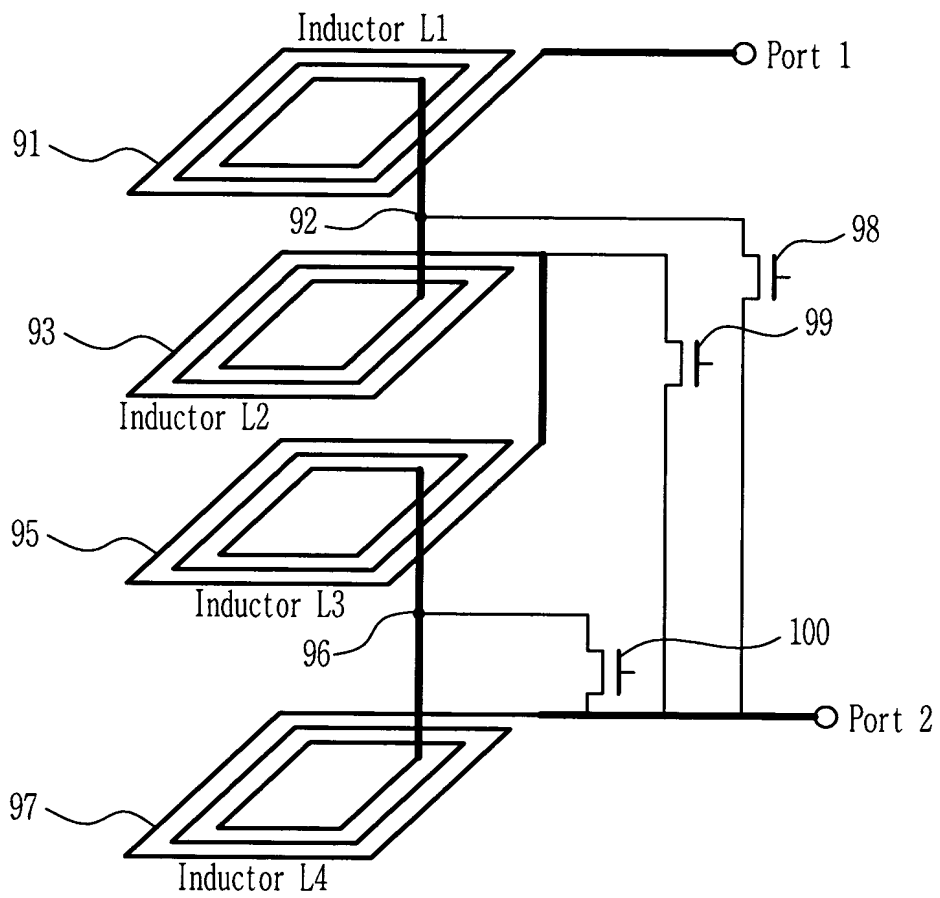


FIG. 12

